

CLAIMS

1. A peak detector for detecting a peak signal, comprising:
an input circuit to input an input signal;
5 a track and hold circuit to hold said input signal and to output said peak signal;
a comparator to compare said input signal and said peak signal to generate a
clock signal;
said track and hold circuit to output said peak signal in accordance with said
clock signal.

10 2. A peak detector for detecting a peak signal as in Claim 1, wherein said
peak detector includes a capacitor to hold said peak signal.